ISO/IEC JTC1/SCl7 **2005-12-16**
Cards and personal identification

ISO/IEC JTC1/SC17 N 2921

DOCUMENT TYPE: Defect Report – Technical Corrigendum

TITLE: Defect Report and Technical Corrigendum 1 for - International Standard ISO/IEC 14443-3:2001/AM1: Identification cards – Contactless integrated circuit(s) cards – Proximity cards – Part 3: initialization and anticollision – Amendment 1: Bit rates for *fc*/64, *fc*/32 and *fc*/16

BACKWARD POINTER: N 2856

SOURCE: Secretariat ISO/IEC JTC1/SC17

STATUS: **The Defect Report has been discussed and agreed by WG8 and is now circulated to SC17 for approval.**

ACTION ID: ACT

WORK ITEM: 1.17.34.1

DUE DATE: 2006-03-16

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G17 Defect Report

DEFECT REPORT

The submitter of a defect report shall complete the items in Part 2 and shall send the form to the Convener or the Secretariat of the WG with which the relevant editor's group is associated.

The WG Convener or Secretariat shall complete the items in Part 1 and circulate the defect report for review and response by the appropriate defect editing group.

The defect editor shall complete Part 4 and submit the completed report to the Convener or the Secretariat of the WG.

PART 1 - TO BE COMPLETED BY WG SECRETARIAT
DEFECT REPORT NUMBER: 1
WG SECRETARIAT: **Belá Gipp**
DATE CIRCULATED BY WG SECRETARIAT: Agreed at the WG8 meeting 2005-09-26/30
DEADLINE ON RESPONSE FROM EDITOR:

PART 2 - TO BE COMPLETED BY SUBMITTER
SUBMITTER: **ISO/IEC JTC1/SC17/WG8**
FOR REVIEW BY: ISO/IEC JTC1/SC17
DEFECT REPORT CONCERNING (number and title of International Standard or DIS final text): **ISO/IEC 14443-3:2001/AM1**
QUALIFIER (e.g. error, omission, clarification required): **Wrong values**
REFERENCES IN DOCUMENT (e.g. page, clause, figure, and/or table numbers): **Page 8, Table Amd. 1-4 – Minimum TR2**
NATURE OF DEFECT (complete, concise explanation of the perceived problem): **The timing requirements of the TR2 defined in Table Amd.1-4 of ISO/IEC 14443-3:2001/AM1 cannot or can hardly be fulfilled by current and future operating systems and smart card chips.**
SOLUTION PROPOSED BY THE SUBMITTER (optional): **see Attachment**

PART 3 - EDITOR'S RESPONSE – The text to correct the defect is attached. It has been approved by WG8.
ANY MATERIAL PROPOSED FOR PROCESSING AS A TECHNICAL CORRIGENDUM TO, AN AMENDMENT TO, OR A COMMENTARY ON THE INTERNATIONAL STANDARD OR DIS FINAL TEXT IS ATTACHED TO THIS COMPLETED REPORT:

G19 Technical Corrigendum Cover Page

INTERNATIONAL STANDARD ISO/IEC 14443-3:2001/AM1
TECHNICAL CORRIGENDUM 1

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INTERNATIONAL ORGANIZATION FOR STANDARDIZATION • ORGANISATION INTERNATIONALE DE NORMALISATIO
INTERNATIONAL ELECTROTECHNICAL COMMISSION • COMMISSION ÉLECTROTECHNIQUE INTERNATIONAL

Correction of the TR2 requirements

TECHNICAL CORRIGENDUM 1

Correction des TR2 besoins

RECTIFICATIF TECHNIQUE 1

Technical Corrigendum 1 to International Standard ISO/IEC 14443-3:2001/AM1 Identification cards – Contactless integrated circuit(s) cards – Proximity cards – Part 3: initialization and anticollision – Amendment 1: Bit rates for *fc*/64, *fc*/32 and *fc*/16was prepared by Joint Technical Committee ISO/IEC JTC 1, Subcommittee SC 17, Cards and Personal Identification, Subcommittee SC 17

Début du texte du rectificatif

ICS 00.000.000 **Ref. No. ISO/IEC 14443-3:2001/AM1/Cor.0:2005(E**

Descriptors: xxxxxxxxxx xxxxxx xxxxxxxx xxxxxxxx xxxxxx, xxxx xxxxxxxxxxxxxxxx xxxxxxxxxxx xxxxxxx xxxxx xxxxx xx.

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Proposed solution for the Defect Report related to ISO/IEC 14443-3:2001/AM1

Replace on page 8 the Table Amd. 1-4, currently:

Table Amd. 1-4 — Minimum TR2 coding

b3	b2	Minimum TR2 for PICC to PCD bit rate of			
		f_c /128 (1 etu = 8/ f_s)	**f_c /64** (1 etu = 4/ f_s)	**f_c /32** (1 etu = 2/ f_s)	**f_c/16** (1 etu = 1/f_s)
0	0	10 etu + 32/f_s	10 etu + 32/f_s	10 etu + 32/f_s	10 etu + 32/f_s
0	1	10 etu + 32/f_s	10 etu + 32/f_s	10 etu + 32/f_s	26 etu
1	0	10 etu + 32/f_s	10 etu + 32/f_s	18 etu	18 etu
1	1	10 etu + 32/f_s	14 etu	14 etu	14 etu

with the text below:

Table Amd. 1-4 — Minimum TR2 coding

b3	b2	Minimum TR2 for PICC to PCD bit rate of			
		f_c /128 (1 etu = 8/ f_s)	**f_c /64** (1 etu = 4/ f_s)	**f_c /32** (1 etu = 2/ f_s)	**f_c/16** (1 etu = 1/f_s)
0	0	10 etu + 32/f_s	10 etu + 32/f_s	10 etu + 32/f_s	10 etu + 32/f_s
0	1	10 etu + 128/f_s	10 etu + 128/f_s	10 etu + 128/f_s	10 etu + 128/f_s
1	0	10 etu + 256/f_s	10 etu + 256/f_s	10 etu + 256/f_s	10 etu + 256/f_s
1	1	10 etu + 512/f_s	10 etu + 512/f_s	10 etu + 512/f_s	10 etu + 512/f_s