

**Disposition of comments on:
CD ISO/IEC 14443-4:2008/PDAM3 — Identification cards —
Contactless integrated circuit cards — Proximity cards — Part 4:
Transmission protocol — AMENDMENT 3: Bit rates of 3fc/4, fc,
3fc/2, 2fc**

Reference documents:

Ballot is in SC17 N 4256 = WG8 N 1830

Ballot Result is in SC17 N 4308 = WG8 N 1844

Project Editor:

Reinhard Meindl, Austria

The following pages provide the details of the comments and detailed information about their resolutions, how WG8 had tried to resolve each received comment from the CD Ballot (PDAM) at the WG8 meeting held in Graz, Austria, on 2012-04-18/20.

According to the advice from the SC17 Secretariat WG8 decided by WG8 Resolution 51.11 (contained in WG8 N 1887) to issue the new text of 14443-4:2008/Amd.3, i.e. WG8 N 1896, for DIS 14443-4:2008/DAM3 balloting.

Template for comments and secretariat observations

Date: 2012-04-19	Document: JTC1/SC17N4256 ISO/IEC 14443-4(2008)/PDAM3
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1	2	(3)	4	5	(6)	(7)
MB ¹	Clause No./ Subclause No./ Annex (e.g. 3.1)	Paragraph/ Figure/Table/Note (e.g. Table 1)	Type of comment ²	Comment (justification for change) by the MB	Proposed change by the MB	Secretariat observations on each comment submitted
JP1	Whole documents		ge	This amendment is for VHBR (bit rates of $3fc/4$ and fc).. It should be avoided in this amendment to make technical change or add new technology which may influence the users who read current ISO/IEC 14443 series (up to 848kbps; revised in 2008 to 2011) and the products compliant to them.	Do not make technical change or add new technology which may influence the users who read current ISO/IEC 14443 series (up to 848kbps; revised in 2008 to 2011) and the products compliant to them.	Accepted
JP2	Whole documents		te	ISO/IEC 14443-4/Amd.3 is related closely with other parts on "VHBR higher than $fc/2$ "; - ISO/IEC14443-2/Amd.5 - ISO/IEC 14443-3/Amd.6 - ISO/IEC 10373-6/Amd.5. However, there are many technical issues in VHBR higher than $fc/2$, especially in ISO/IEC14443-2/Amd.5 (see the ballot comments of ISO/IEC14443-2/Amd.5). If the ballot on one part of VHBR higher than $fc/2$ does not keep pace with other parts of VHBR higher than $fc/2$, there might be a technical mismatch among the parts of VHBR higher than $fc/2$	Synchronize discussion with other parts of ISO/IEC 14443 (including test methods).	Accepted
JP3	Whole documents		ge	It is not clear at PDAM ballot stage whether there are any known patents regarding ISO/IEC 14443-4:2008/Amd.3. Even though ISO/IEC takes no position concerning the evidence, validity and scope of the patents, the patents known at this time should be disclosed.	Disclose the patent list regarding ISO/IEC 14443-4:2008/Amd.3.	Accepted
JP4	Whole documents		te	Before the test method for VHBR is fixed, it is difficult to determine parameters for VHBR. The test method for VHBR should be developed concurrent with ISO/IEC	Prior to the next ballot stage, review the test method for VHBR.	Accepted

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NOTE Columns 1, 2, 4, 5 are compulsory.

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				14443-4:2008/Amd.3 to confirm its reproducibility and feasibility.		
UK1	Page ii	Copyright notice	ed	ISO copyright notice reminder to be filled in	Also Note to ISO. Correct spelling in ISO template Replace manger with manager	Accepted
UK2	General		ed	As this amendment introduces is a new technique (akin to the Type A Type B situation several years ago) it should be clearly recognisable in the standard as significantly different from existing techniques. The hope is that this will avoid the very real possibility of confusion and possible fragmentation of the market place.	The substance of the proposed changes for this new technique should be contained in a separate ANNEX (Type X) within each part of the specification. Where changes are inserted in the main body of the standard these should reference the Type X ANNEX.	Accepted
FR1	Title	Front page and 1 st page	ED	This amendment only deals with PCD to PICC communication	Replace "Bit rates of 3fc/4 and fc" with "Bit rates of 3fc/4 and fc from PCD to PICC" Replace "Débits binaires de 3 fc/4 et fc" with "Débits binaires de 3 fc/4 et fc de PCD vers PICC"	Accepted
AT1	Title and Page 6	9	TE	PSK can also be used for even higher bit rates of 20 and 27 Mbps	Add also 3 fc/2 and 2fc	Resolved By WG8 resolution considering bit rates higher than 10 Mbps
JP5	9		te	JNB (Japan National Body) recognizes "Page 6" in this sentence means "Page 6, Clause 9 in ISO/IEC 14443-4/Amd.2". However, Table 6 ISO/IEC 14443-4/Amd.2 is for the transmission from PCD to PICC .In PDAM 14443-4/Amd.3, there is a lack of consideration regarding the	Clarify the scope regarding supported bit rate(s) of both direction (PCD to PICC / PICC to PCD)	Resolved by Updating figure in amd 2 to make clear that PSK method is only used for direction PCD to PICC See revised table D.1 and

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				transmission from PICC to PCD.		figure D.1
FR2	9		ed	ISO rules: Clause X is referred to by "Clause X" Subclause X.Y is referred to by "X.Y" only	Replace "Page 6, 9" with "Page 6, Clause 9"	Resolved by FR3
FR3	9		ED	Clause 9 has been added in a pending amendment	Either replace "Page 6, 9" with "Page 6, Clause 9 of ISO/IEC 14443-4/FPDAM2" or add an editor's note which states that Clause 9 is in pending amendment FPDAM2	Accepted By using "Clause 9 of 14443-4/FPDAM2"
FR4	9	Table 6	ED	The precise modification of Table 6 is not given	Clarify new Table 6	Resolved by JP5

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New table 5 and figure

Table 5 — Function tags identifier definition

Tag (Hex)	Description	Length (Hex)	Value		
			Tag (Hex)	Length (Hex)	Value
'A1'	Bit rates Request	'0'			
'A2'	Bit rates Indication	L	'80'	'02'	supported bit rates from PCD to PICC 1 st byte is specified in Figure 23 2 nd byte is specified in Figure Amd.xx
			'81'	'02'	supported bit rates from PICC to PCD 1 st byte is specified in Figure 23

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Tag (Hex)	Description	Length (Hex)	Value		
					2 nd byte set to '00', other values are RFU
			'82'	'01'	supported framing options PICC to PCD (see Figure 24)
'A3'	Bit rates Activation	L	Tag (Hex)	Length (Hex)	Value
			'83'	'02'	selected bit rate from PCD to PICC ^a 1 st byte is specified in Figure 23 2 nd byte is specified in Figure Amd.xx
			'84'	'02'	selected bit rate from PICC to PCD ^a 1 st byte is specified in Figure 23 2 nd byte set to '00', other values are RFU
			'85'	'01'	selected framing options PICC to PCD (see Figure 24)
'A4'	Bit rates Acknowledgement	'0'			

^a The PCD shall set only one bit. The PCD shall not activate simultaneously a bit rate higher than $fc/16$ for PCD to PICC communication and a bit rate of $fc/128$ for PICC to PCD communication in Type A.

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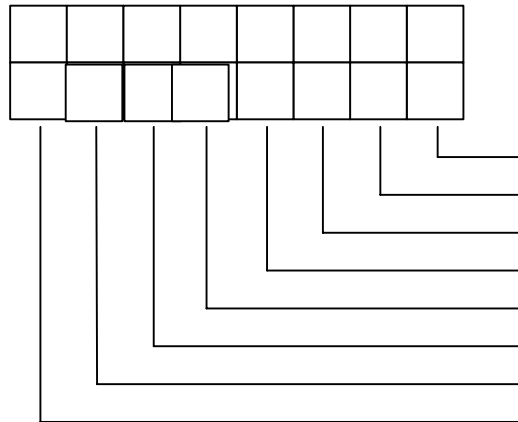


Figure Amd.xx — 2st byte: Coding of bit rates

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