



ISO/IEC JTC 1/SC 17/WG 8 N 2093

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Integrated circuit cards without contacts

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Secretariat: DIN

TF3 N163 Minutes 14th TF3 meeting London 2013

Date of document	2013-07-22
Expected action	Info
Source	TF 3 Convener

1. The task force agreed that whatever happens we will need to issue one or more NPs to inform the broader ISO community of what we are doing.
2. Chris agreed to formulate NP(s) and after review by TF3 submit them via UK BSI to ISO the UK expects support from France, Germany, Austria and Switzerland
3. Summary of NP Items

Ref	Title	Supporting document(s)	Project editor
NP1	Clarification of 15693-3	ISO_IEC_SC17_WG8_TF3_N0159	Kostas
NP2	Adding security framework to 15693	ISO_IEC_SC17_WG8_TF3_N0161	Jose
NP3	Extended VICC memory organisation for 15693	ISO_IEC_SC17_WG8_TF3_N0157	Daniel

4. Project plan

Ref	Title	First CD	Publication	Project editor
AMD2	Clarification of 15693-3	Q4 2013	2015	Kostas
AMD3	Extended VICC memory organisation for 15693	1Q 2014	2015	Daniel
NP3	Adding security framework to 15693	1Q 2014	2015	Jose

5. Action on all

Determine Maximum and minimum FWT

6. TF3 Next meetings

Short meeting in Singapore during 23-27th Sept

Full meeting 15-16th October in London

7. Current membership of TF3

Jose Perez	JPerez@emmicroelectronic.com	Switzerland	agreed to be Project editor for AMD 4
Reinhard Meindl	reinhard.meindl@nxp.com	Austria	
Kostas Aslanidis	k-aslanidis@ti.com	Germany	agreed to be Project editor for AMD 2
Daniel Orsatti	daniel.orsatti@st.com	France	agreed to be Project editor for AMD 3
Klaus Finkenzeller	Klaus.Finkenzeller@gi-de.com	Germany	
Hideaki YAMAMOTO	yamamoto.hideaki@lab.ntt.co.jp	Japan	
Chris Stanford	chris@cjsconsultancy.com	UK	Convener WG8 TF3 Vicinity card